

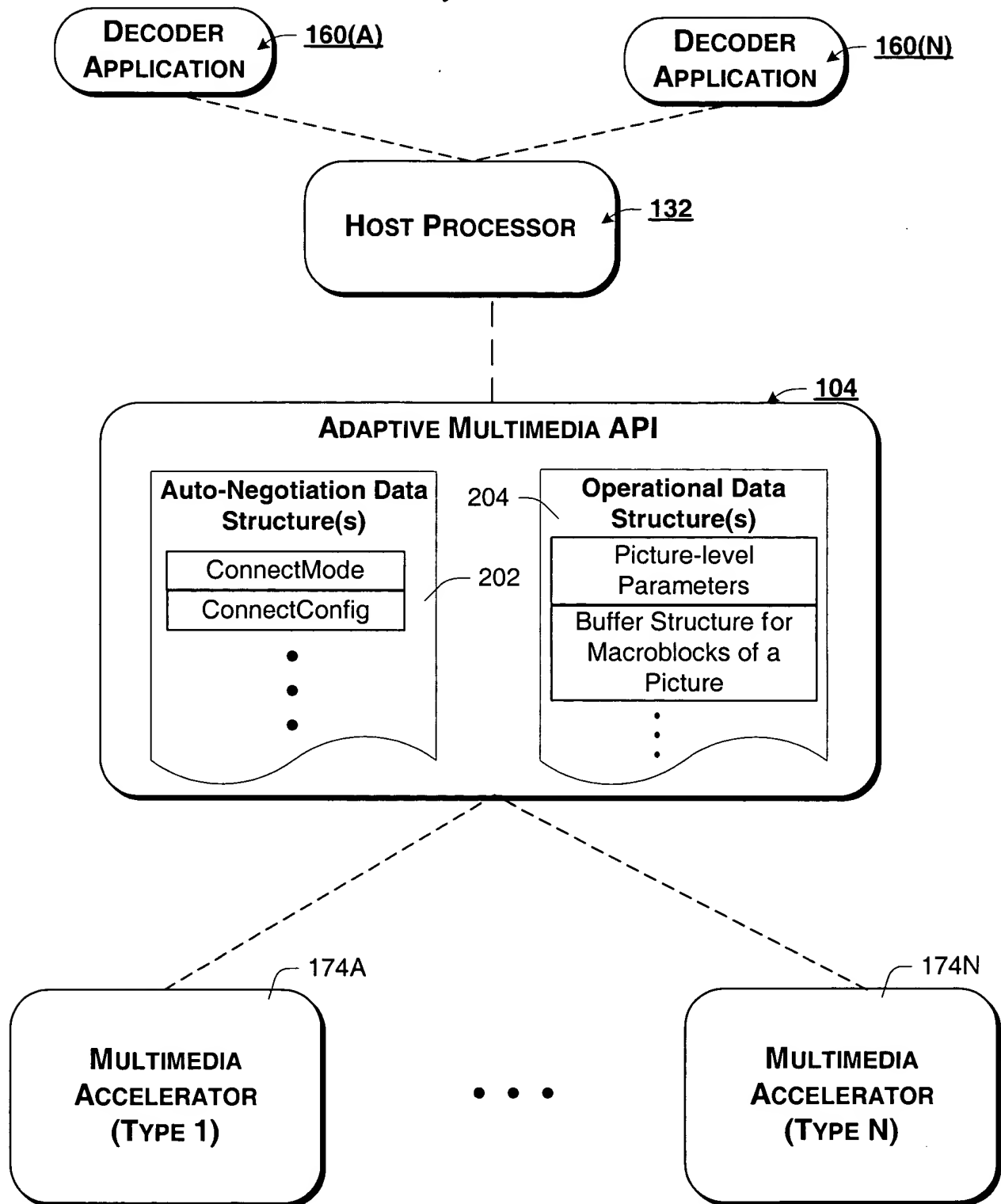
Fig. 2

Fig. 3

300

302		304	306
MB Address	RD Pointer	Control Operation	
...			

Fig. 4

400

402	Residual Difference Data
	Data for MB 3
	Data for MB 1
	Data for MB 2
	...

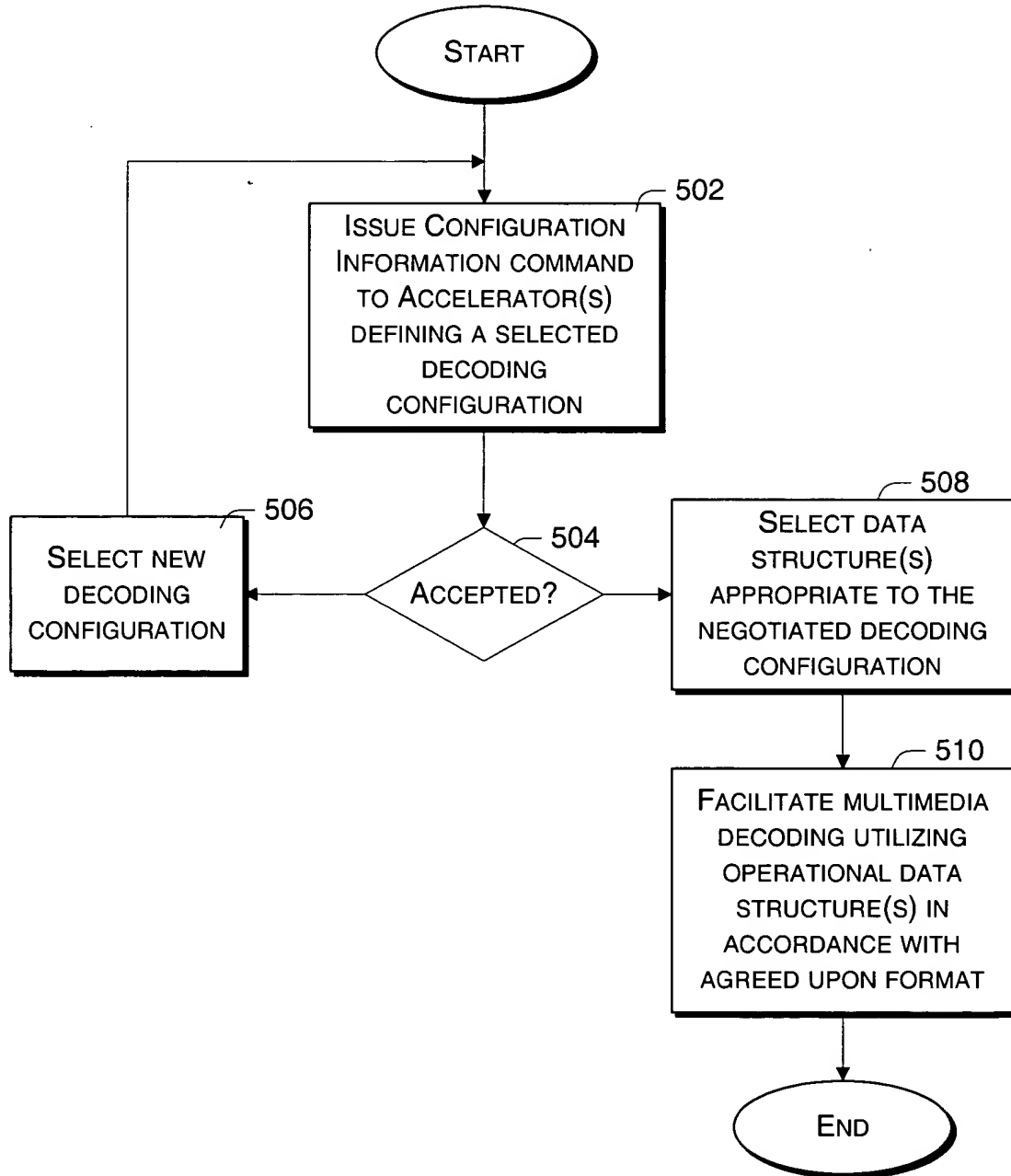
*Fig. 5*500

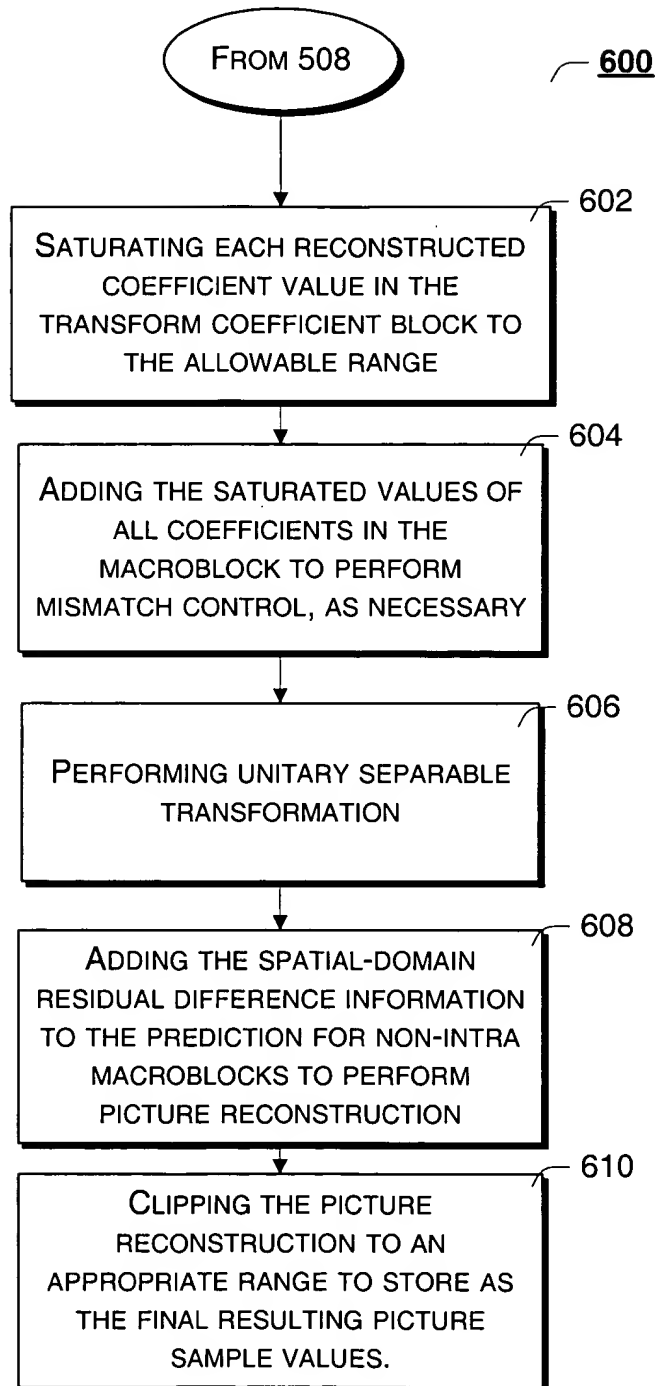
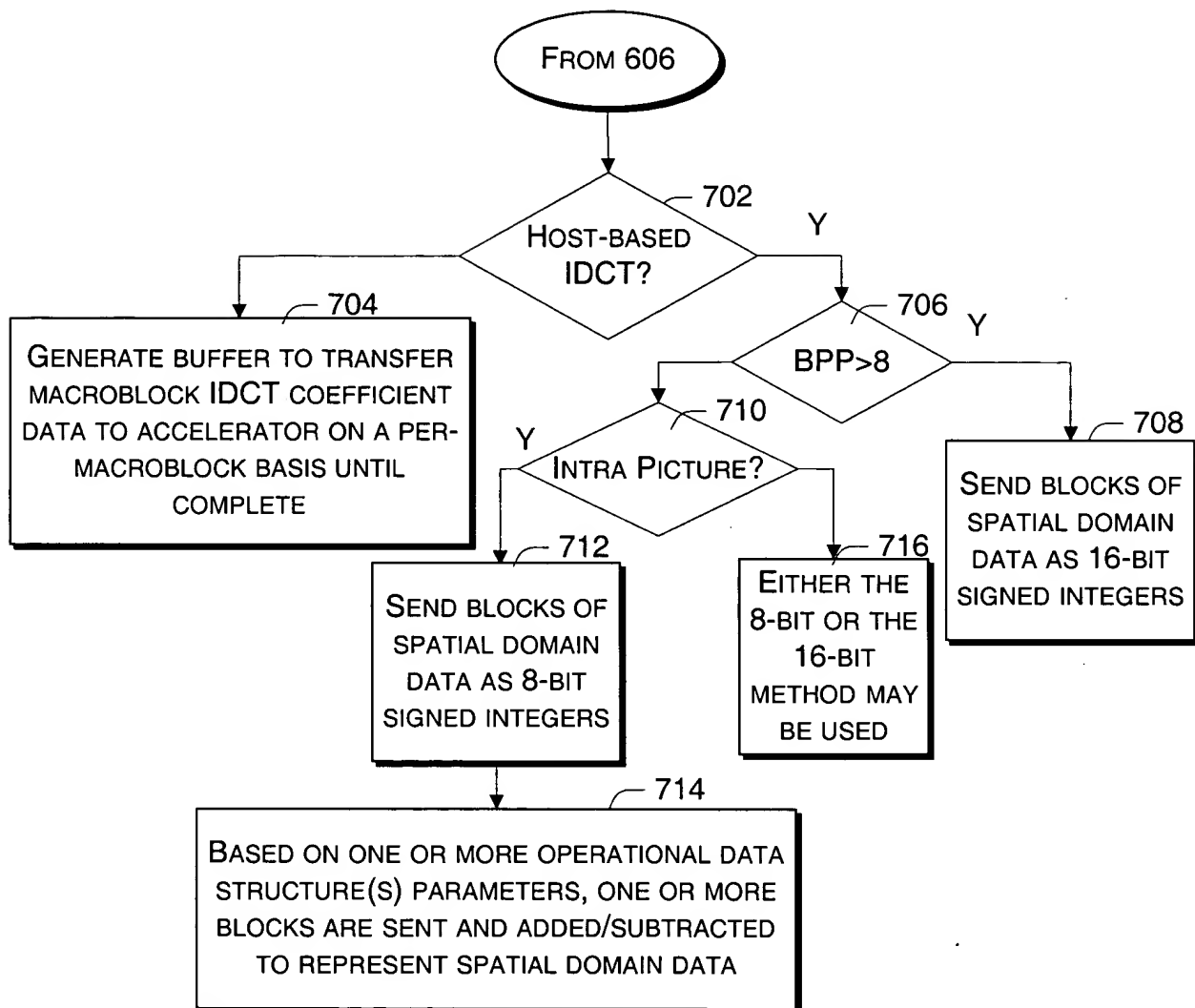
Fig. 6

Fig. 7

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graph TD; 802[API ANALYZES RECEIVED COMMAND FROM DECODER TO GENERATE DATA STRUCTURE TO PASS TO ACCELERATOR] --> 804[API GENERATES A DATA STRUCTURE INCLUDING DEBLOCKING FILTER CONTROL COMMANDS BASED, AT LEAST IN PART, ON RECEIVED COMMAND FROM DECODER]; 804 --> 806[API PASSES DATA STRUCTURE TO ACCELERATOR INCLUDING DEBLOCKING FILTER CONTROL COMMANDS]; 806 --> 808[ACCELERATOR MODIFIES ZERO OR MORE DEBLOCKING FILTER PARAMETERS IN ACCORDANCE WITH RECEIVED DATA STRUCTURE];
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802

API ANALYZES RECEIVED
COMMAND FROM DECODER TO
GENERATE DATA STRUCTURE TO
PASS TO ACCELERATOR

804

API GENERATES A DATA
STRUCTURE INCLUDING
DEBLOCKING FILTER CONTROL
COMMANDS BASED, AT LEAST IN
PART, ON RECEIVED COMMAND
FROM DECODER

806

API PASSES DATA STRUCTURE TO
ACCELERATOR INCLUDING
DEBLOCKING FILTER CONTROL
COMMANDS

808

ACCELERATOR MODIFIES ZERO OR
MORE DEBLOCKING FILTER
PARAMETERS IN ACCORDANCE
WITH RECEIVED DATA STRUCTURE

Fig. 9

900 →

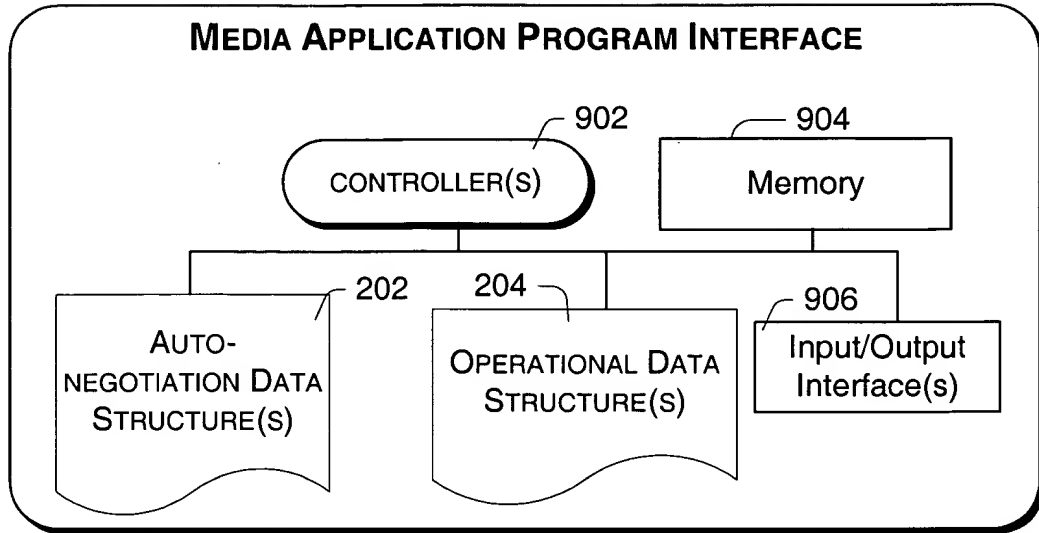


Fig. 10

→ 1000

